

DIALOG(R)File 352:Derwent WPI

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009647026 **Image available**

WPI Acc No: 1993-340575/199343

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Substrate for liquid crystal display - has single crystal semiconductor film having light transmission holes on transparent substrate, and light shielding layer covering elements NoAbstract

Patent Assignee: SHARP KK (SHAF)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 5249496	A	19930928	JP 9284609	A	19920305	199343 B

Priority Applications (No Type Date): JP 9284609 A 19920305

Patent Details:

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Title Terms: SUBSTRATE; LIQUID; CRYSTAL; DISPLAY; SINGLE; CRYSTAL;
SEMICONDUCTOR; FILM; LIGHT; TRANSMISSION; HOLE; TRANSPARENT;
SUBSTRATE; LIGHT; SHIELD; LAYER; COVER; ELEMENT; NOABSTRACT

Derwent Class: P81; U14

International Patent Class (Main): G02F-001/136

International Patent Class (Additional): H01L-027/12; H01L-029/784

File Segment: EPI; EngPI

DIALOG(R)File 347:JAPIO

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04257796 **Image available**

SUBSTRATE FOR LIQUID CRYSTAL DISPLAY AND ITS PRODUCTION

PUB. NO.: 05-249496 [JP 5249496 A]

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(ELECTRONICS -- Solid State Components)

JAPIO KEYWORD:R011 (LIQUID CRYSTALS); R097 (ELECTRONIC MATERIALS --

Metal Oxide Semiconductors, MOS); R119 (CHEMISTRY -- Heat Resistant

Resins); R124 (CHEMISTRY -- Epoxy Resins)

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ABSTRACT

PURPOSE: To obtain TN system or the like having high contrast even in a structure using a single crystal semiconductor thin film.

CONSTITUTION: This liquid crystal display substrate is used for an active matrix liquid crystal display, comprising a plastic transparent plate 30 and a single crystal silicon film 10 having a hole 18 to transmit light on the rear surface of the plate 30. On the single crystal silicon film 10, MOB transistor .alpha. is formed as one structural component of the liquid crystal driving circuit. A metal film 50 as a light shielding layer is formed between the MOS transistor .alpha. and the plastic transparent plate 30 to cover the MOS transistor .alpha..